



1374.43253X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. TAKAHASHI, et al.
Application No.: 10/722,461
Filed: November 28, 2003
For: SEMICONDUCTOR MEMORY DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: 2827
Examiner: S. MAI

AMENDMENT

Mail Stop: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 28, 2006

Sir:

In response to the outstanding non-final Official Action, dated September 29, 2005, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Substitute Specification;

Amendments to the Claims;

Amendments to the Drawings; and

Remarks are included following the amendments. An Appendix including both annotated and amended drawing Figs. 6, 7 and 11 is attached following the Remarks.